

APR 21 2005

PTO/SB/08a (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1

of

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Complete If Known

Application Number	09/802,168
Filing Date	March 8, 2001
First Named Inventor	Takashi SAIDA
Art Unit	2882
Examiner Name	Kao, Chih Cheng G.
Attorney Docket Number	44471/254519

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number - Kind Code ² (if known)				
<i>[initials]</i>	1	US-5,786,913	7-28-1998	Pfeiffer	cl	sub
<i>[initials]</i>	2	US- 5,239,598	8-24-1993	Wight et al.		
<i>[initials]</i>	3	US- 5,828,472	10-27-1998	Masetti		
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FOREIGN PATENT DOCUMENTS

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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
<i>[initials]</i>	4	JP56-135827	10-23-1981	Mitsubishi Electric Corp		Ab
<i>[initials]</i>	5	EP 0 297 851 A	1-4-1989	Nippon Telegraph and Telephone Corporation		X
<i>[initials]</i>	6	EP 0 255 270 A	2-3-1988	Nippon Telegraph and Telephone Corporation		X

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Sheet 2





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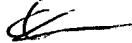
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Art Unit	2882
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	7	I. GLESK ET AL., "Demonstration of ultrafast all-optical packet routing", ELECTRONICS LETTERS, IEE STEVENAGE, GB, Vol. 33, No. 9, 24th April 1997, Pages 794-795, XP006007361	
	8	L.B. SOLDANO ET AL., "Optical Multi-Mode Interference Devices Based on Self-Imaging: Principles and Applications", Journal of Lightwave Technology, IEEE, New York, US, Vol. 13, No. 4, April 1, 1995, Pages 615-627, XP000513578, ISSN: 0733-8724	
	9	MASETTI F. ET AL., "Optical Fiber Buffer for High-Performance Broadband Switching", European Transactions on Telecommunications and Related Technologies, AEI, Milano, IT, Vol. 4, No. 6, 1 November 1993 (1993-11-01), pages 671-679, XP000433724, ISSN: 1120-3862	
	10	PATTAVINA A., "Nonblocking Architectures for ATM Switching", IEEE Communications Magazine, IEEE Service Center, Piscataway, N.J., US, Vol. 31, No. 2, 1 February 1993 (1993-02-01), pages 38-48, XP000334602, ISSN: 0163-6804	

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